

FIG. 1 (PRIOR ART)

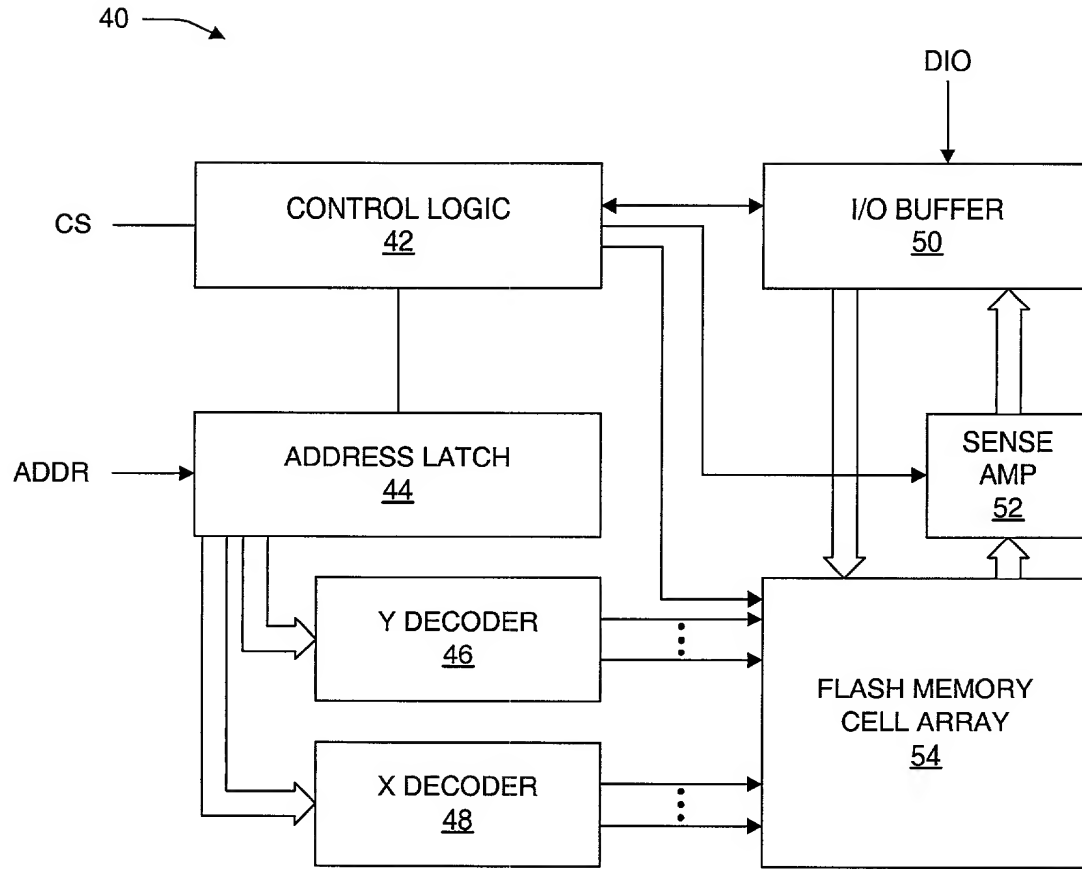


FIG. 2 (PRIOR ART)

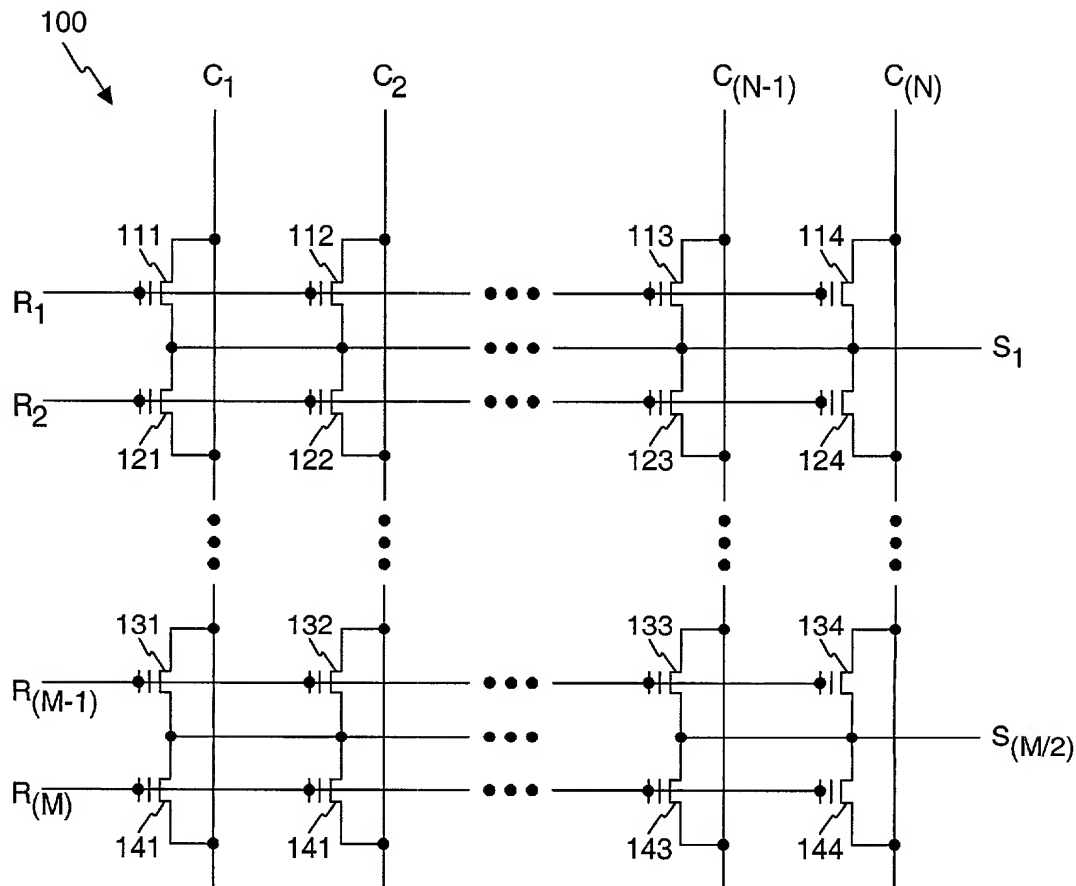


FIG. 3

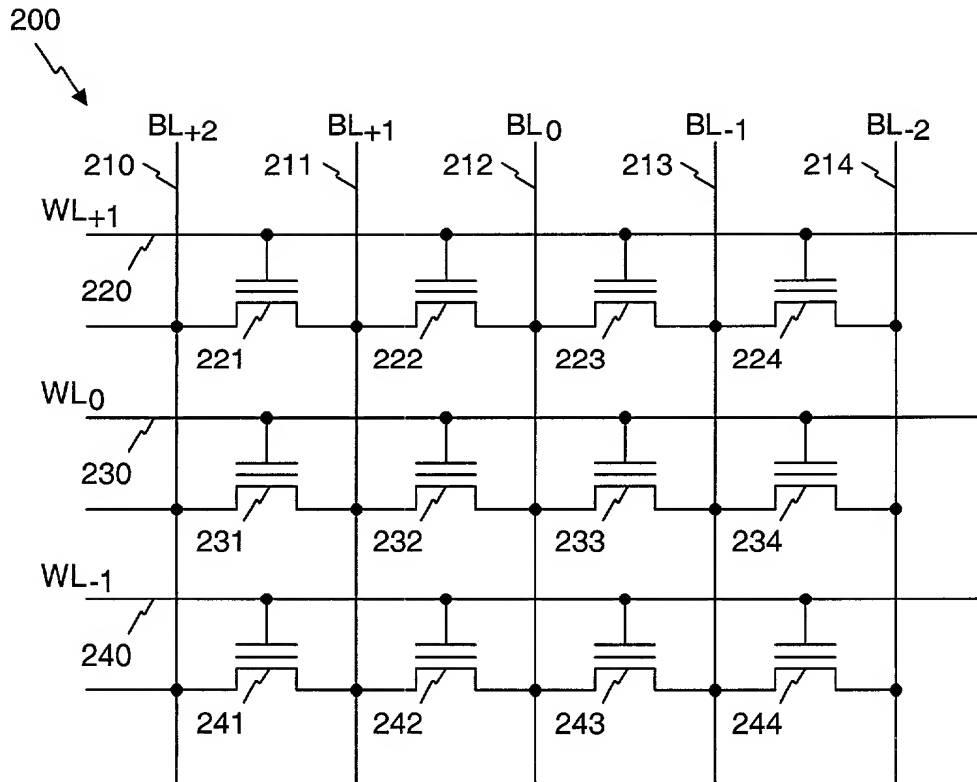


FIG. 4

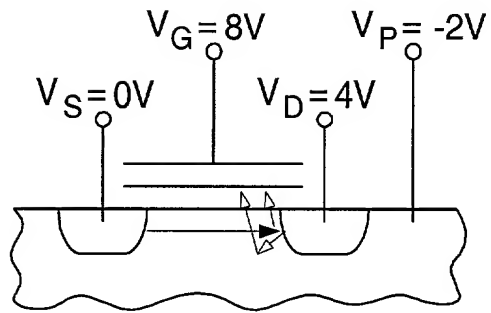


FIG. 5

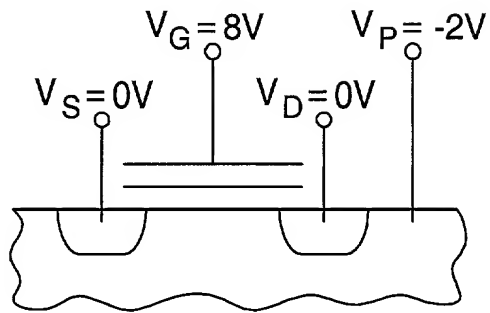


FIG. 6

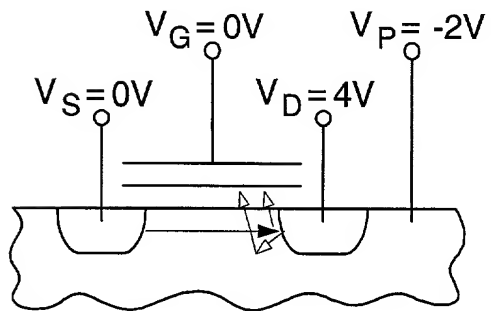


FIG. 7

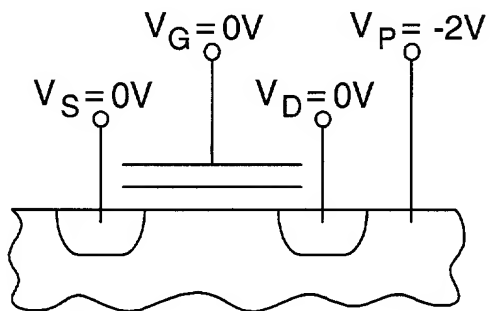


FIG. 8

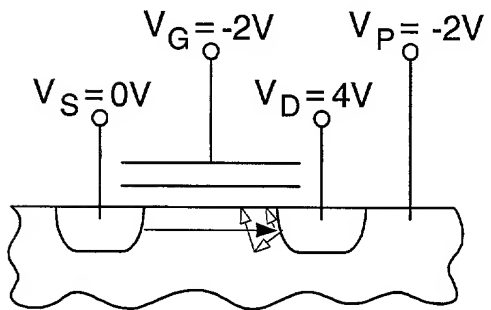


FIG. 9

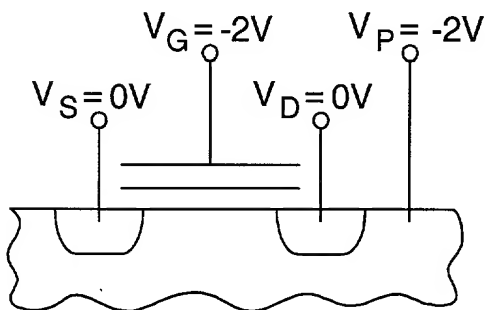


FIG. 10

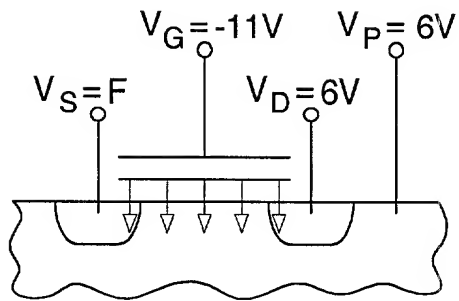


FIG. 11

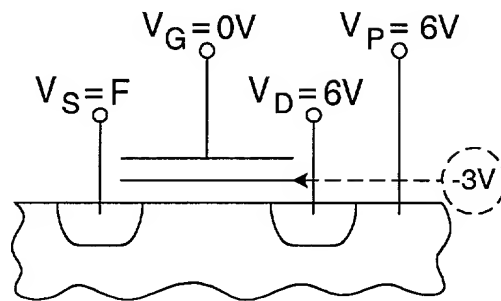


FIG. 12

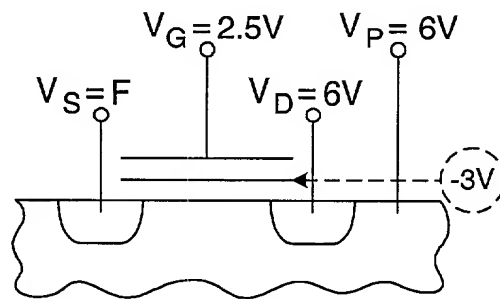


FIG. 13

OPERATION	V _G SELECT	V _G UNSEL	V _S	V _D	V _P	V _{NW}	V _T
READ	2.5V TO 3V	-2V	0V	1V TO 1.5V	0V	0V	
PAGE PROGRAM	7V TO 8V	-2V	0V	4V	-2V	0V	5.5V
PAGE ERASE	-11V	2.5V TO 3V	FOR 6V	6V	6V	6V	2V

FIG. 14

Title: METHOD AND APPARATUS FOR MULTIPLE BYTE
OR PAGE MODE PROGRAMMING AND READING
OF A FLASH MEMORY ARRAY

Inventor(s): Steve K. Hsia et al.

Docket No.: 11030.02

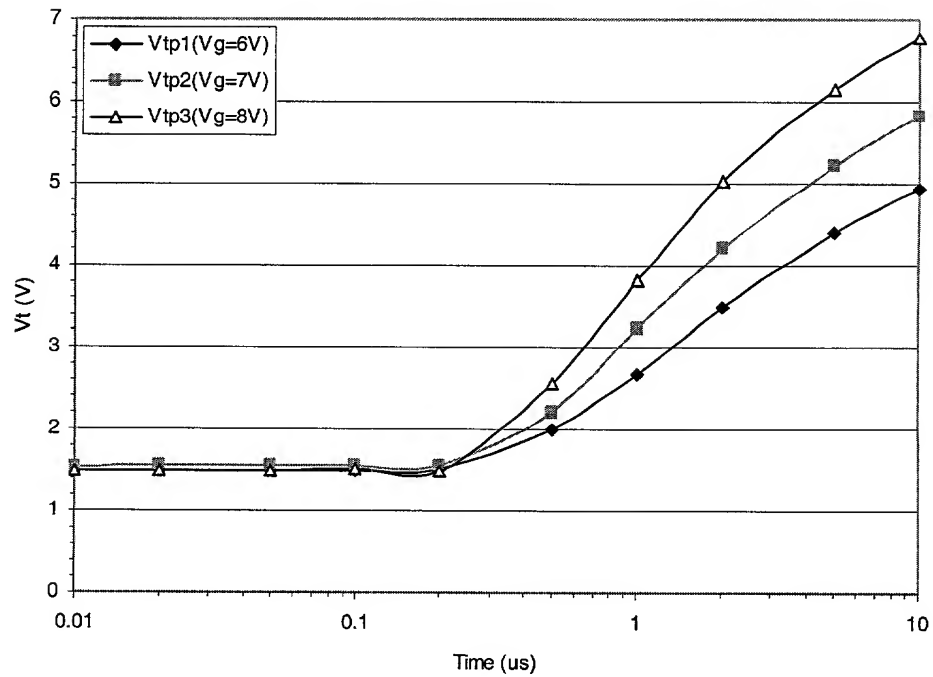


FIG. 15

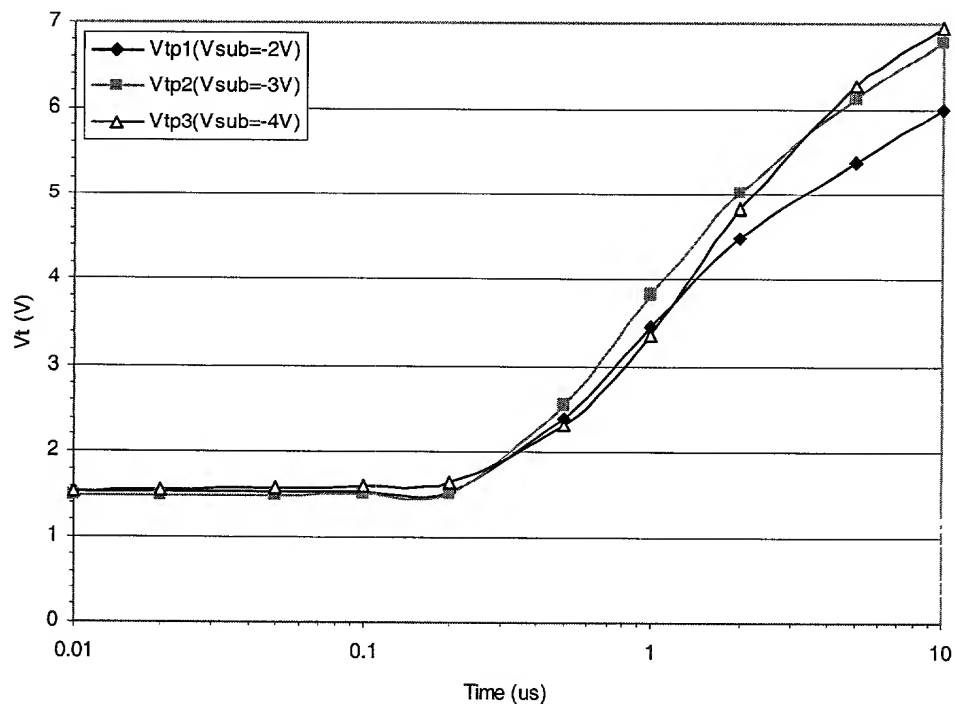


FIG. 16

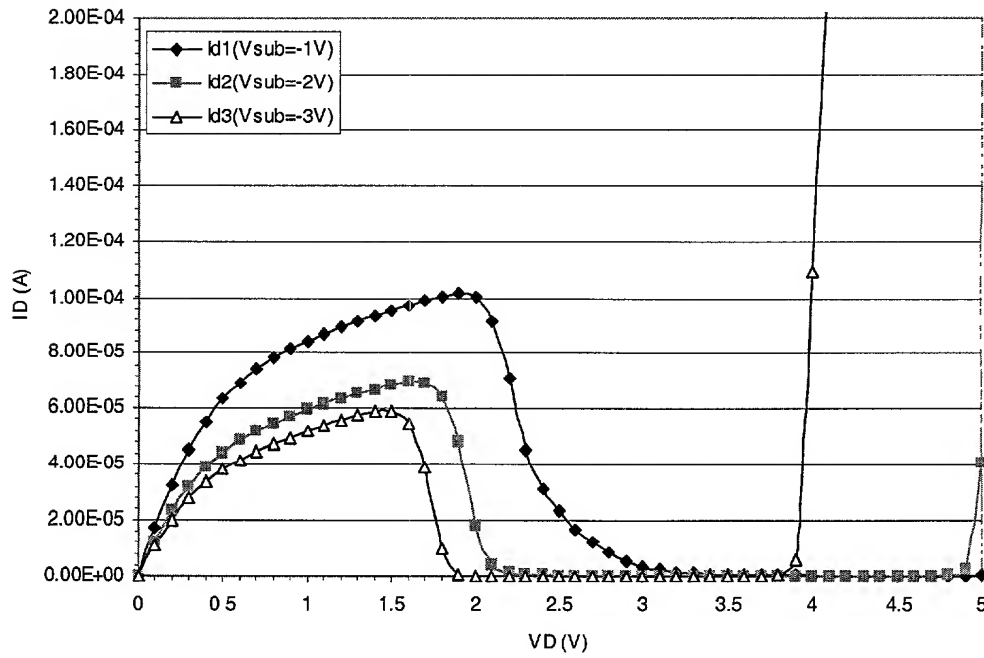


FIG. 17

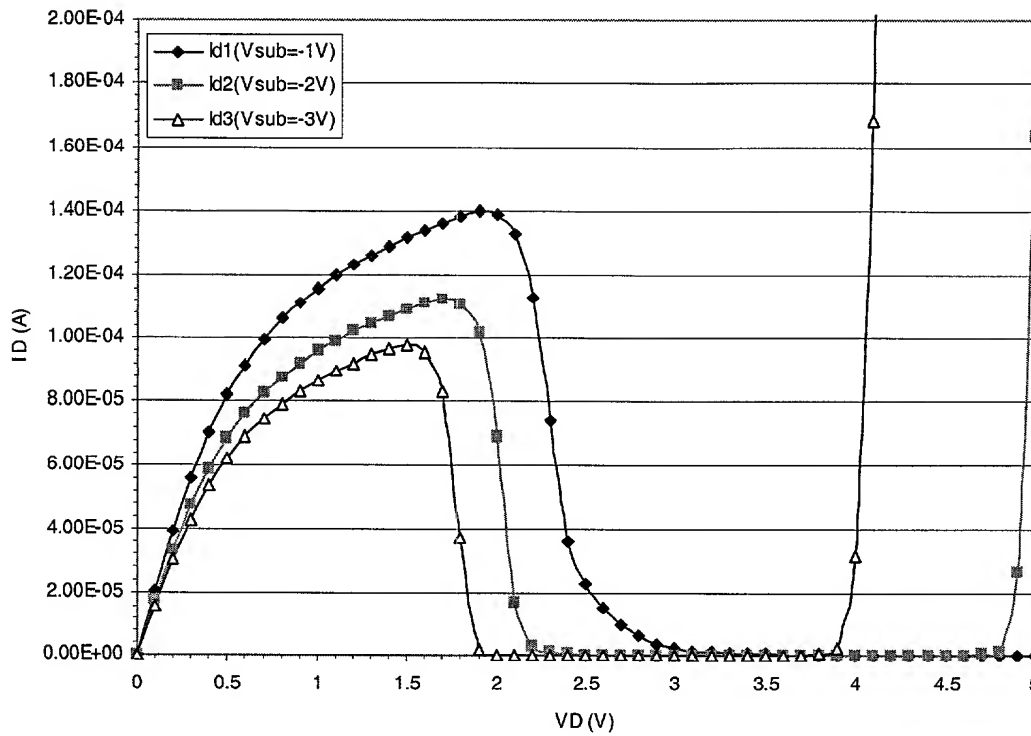


FIG. 18

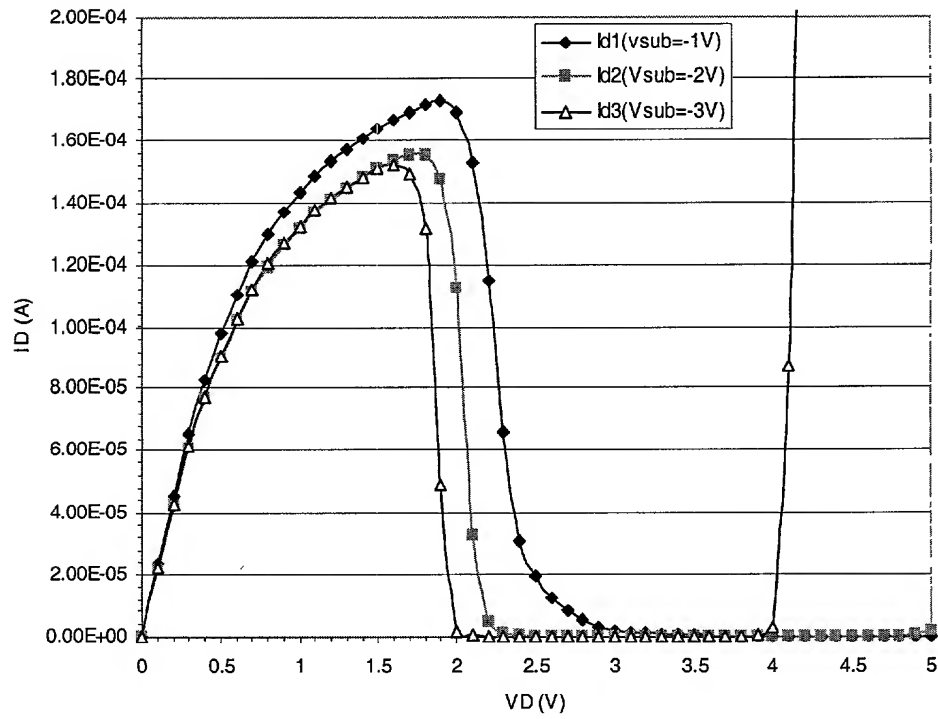


FIG. 19

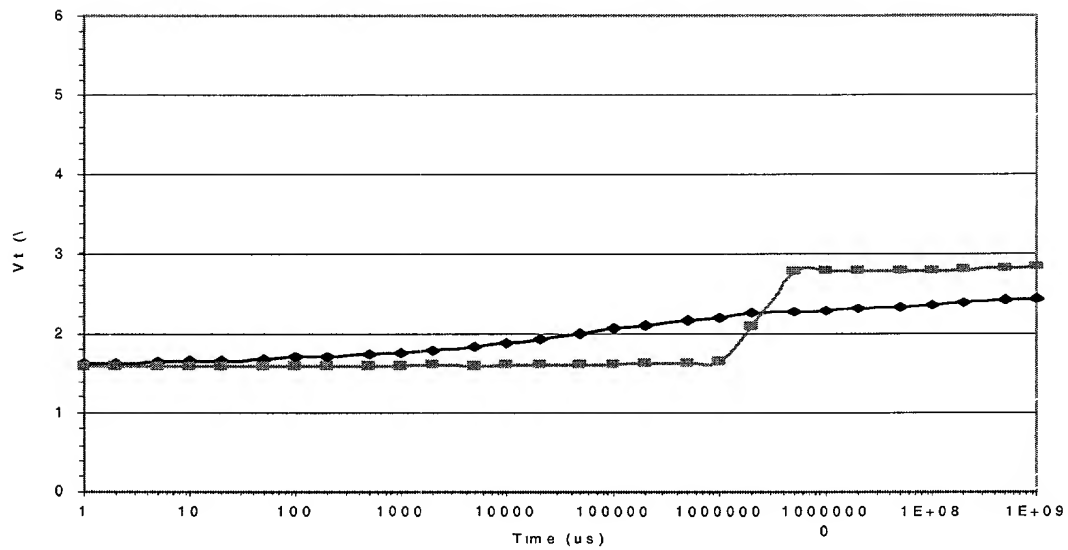


FIG. 20

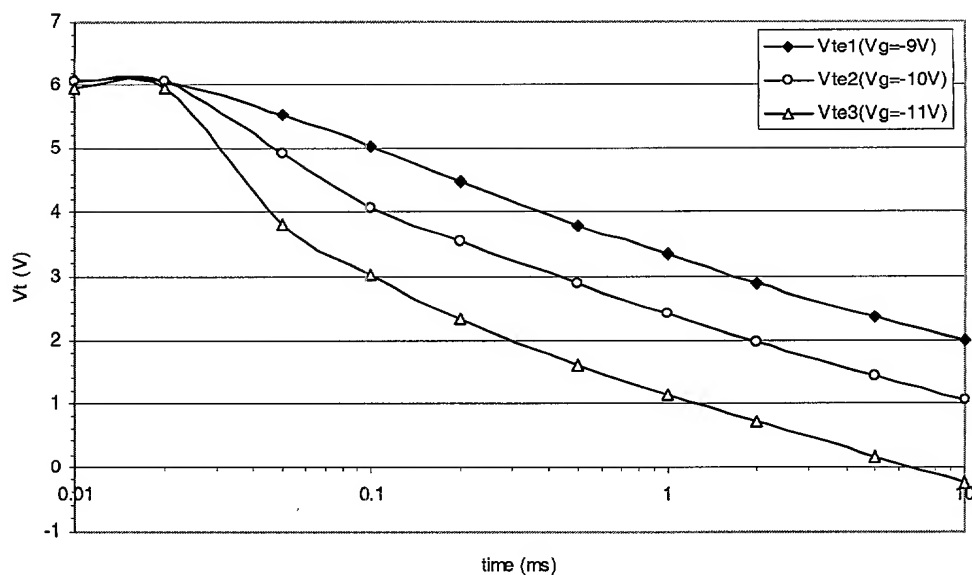


FIG. 21

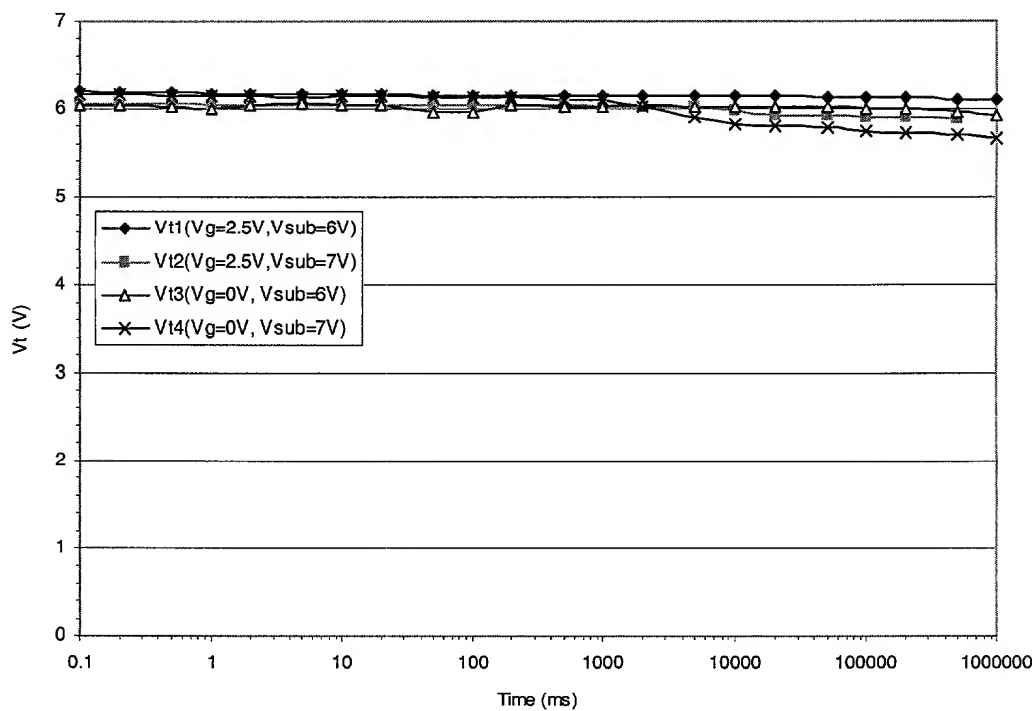


FIG. 22